

*Approved
P5
2/2/06*

FIG. 1 PRIOR ART

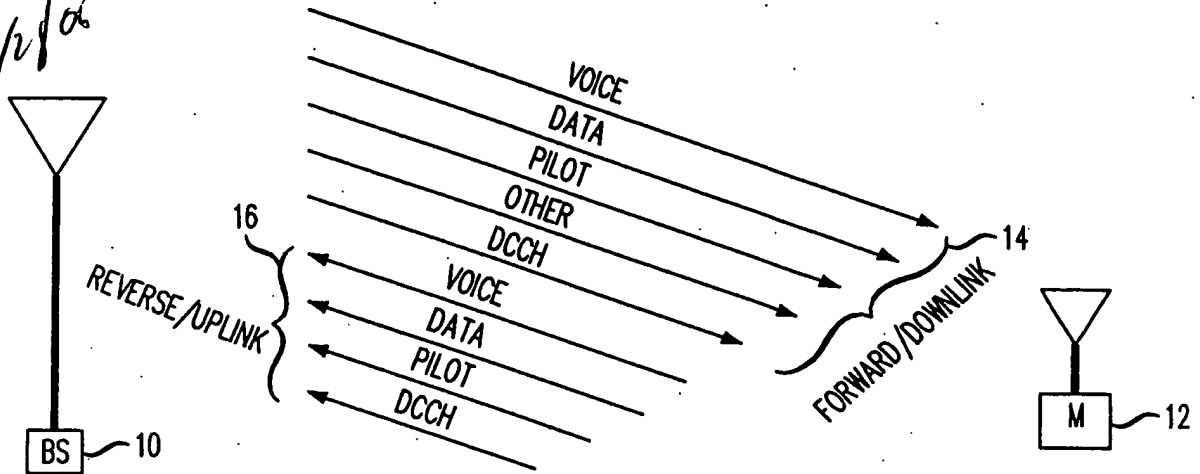


FIG. 2 PRIOR ART

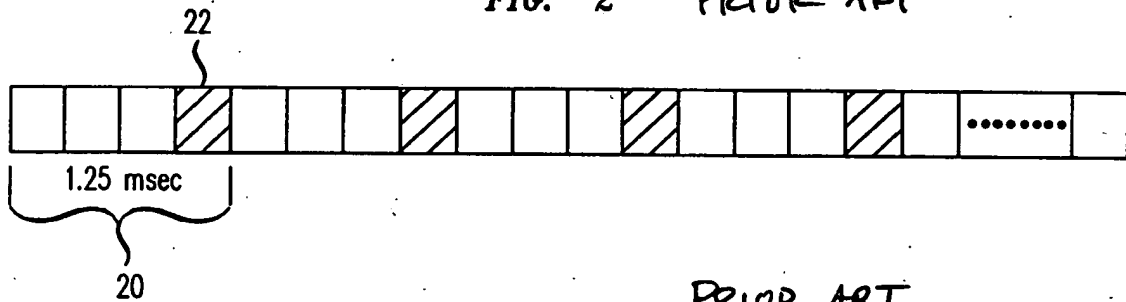


FIG. 3 PRIOR ART

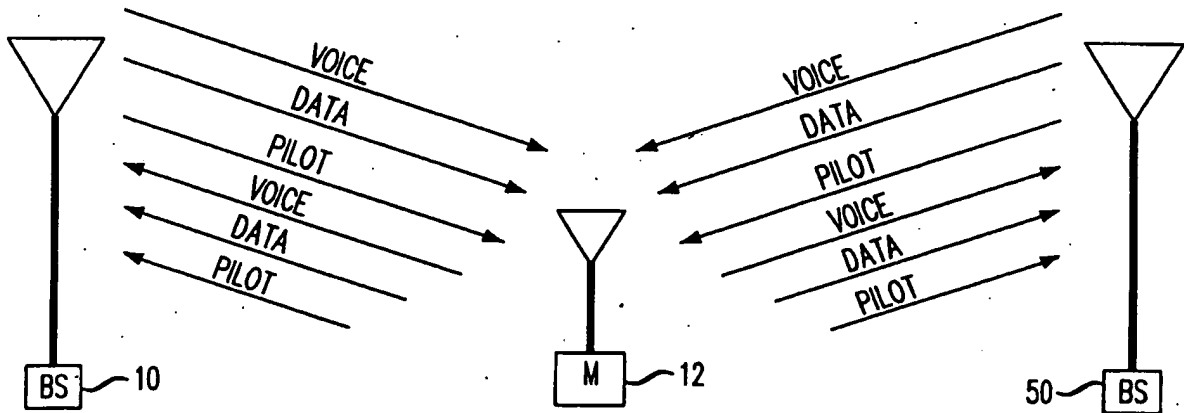


FIG. 4 PRIOR ART

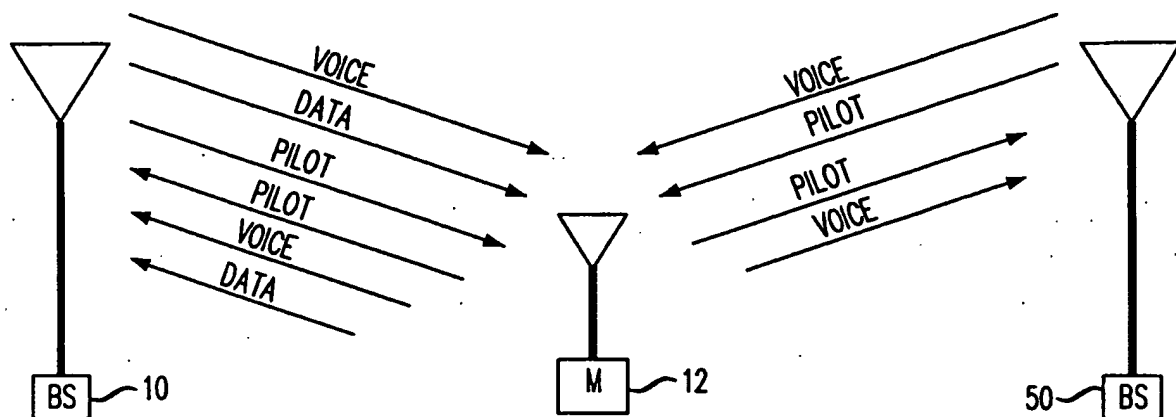


FIG. 5

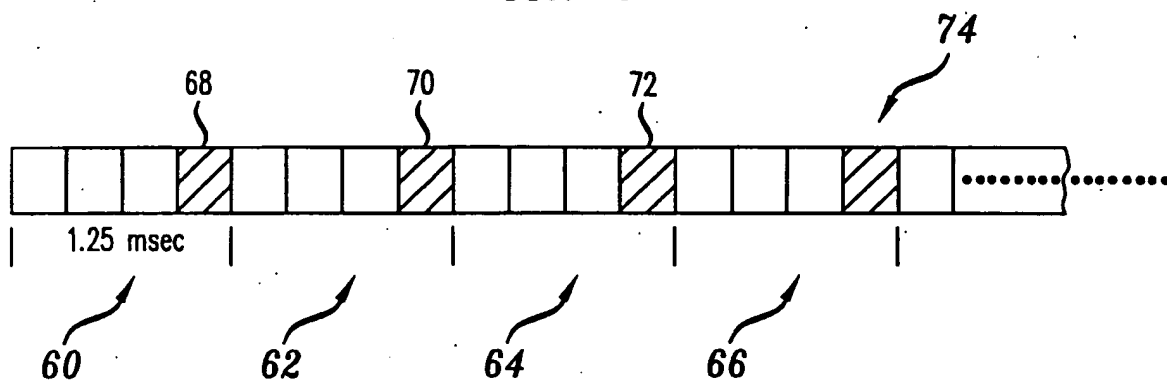
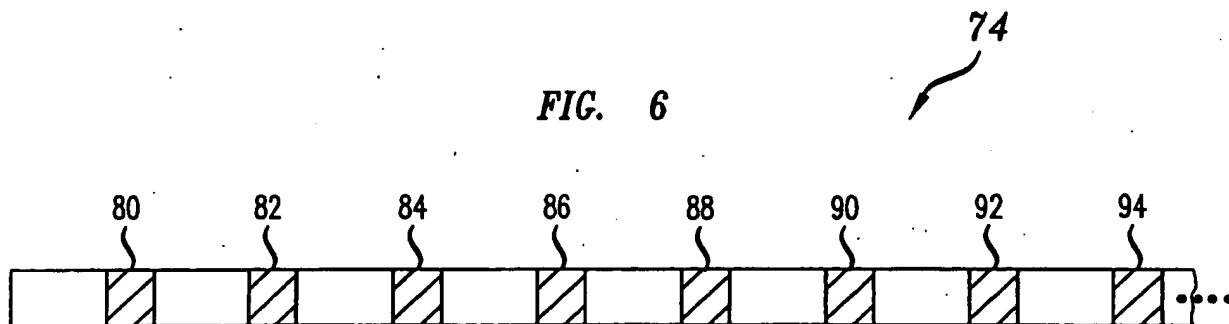


FIG. 6



CONFIGURATION NAME		POWER CONTROL BIT PATTERN (EXAMPLE)			
	V50 D750	VDDD	DDDD	DDDD	DDDD
	V100 D700	VDDD	DDDD	VDDD	DDDD
	V150 D650				
	V200 D600	VDDD	VDDD	VDDD	VDDD
	V250 D550				
V ≡ VOICE	V300 D500	VDVD	VDDD	VDVD	VDDD
D ≡ DATA	V350 D450				
	V400 D400	VDVD	VDVD	VDVD	VDVD
	•				
	•				
	•				
	•				
	•				
	V750 D50	V D V V	V D V V	V D V V	V D V V